CENTRE FOR MATERIALS FOR ELECTRONICS TECHNOLOGY (C-MET)

Memorandum Of Association

Certified to be a correct

An Autonomous Scientific Society of Department of Information Technology
Ministry of Communications & Information Technology, Government of India
Electronics Niketan, 6, CGO Complex, Lodhi Road
NEW DELHI – 110 003

MARCH 2005

P. L. Sadanandan
Registrar
Centre for Materials for Electronics Technology
Panchkula, Off. Paschim Road,
MEMORANDUM OF ASSOCIATION

OF

CENTRE FOR MATERIALS FOR ELECTRONICS TECHNOLOGY (C-MET)

1. The name of the Society shall be: CENTRE FOR MATERIALS FOR ELECTRONICS TECHNOLOGY hereinafter to be referred as 'Society' or 'C-MET'.

2. The Registered Office of the Society shall be situated at CGO Complex, Lodhi Road, New Delhi - 110 003 and it will remain in the State of Delhi.

3. The objects:

3.1 The objectives for which the Society is established are:

3.1.1 To undertake research technology development and production plant activities on electronic materials leading to new scientific and technological knowledge, new phenomenon materials applications: production technology for items for import substitution, export promotion and utilization of natural resources; indigenous supply for critical materials in small quantities to laboratories and user industry.

3.1.2 To promote and coordinate at National level the materials science and engineering efforts to meet the present and future requirements of electronics industry through optimum utilization of resources, expertise and infrastructure.

3.1.3 To contribute directly or indirectly to the efforts of R&D and production agencies in the country in their achieving state-of-art competence in selected areas of materials for electronics technology.

3.1.4 To assist and help the industry and R&D in their indigenisation efforts in electronic materials by way of providing advice, information, consultancy, technology, marketing another inputs and also providing facilities for manpower training, testing and characterization of materials.

3.1.5 To cause the country to attain technological competence and self-reliance in the field of materials for electronics technology so as to reduce vulnerability in development, design, manufacture and export and to acquire freedom and technology strength for exploitation of natural resources in the country's advantage and for choosing better alternative materials to suit Indian conditions.

3.2 With a view to attaining the objectives set out in Para 3.1, the Society may:
3.2.1 Examine and evaluate current science, technology and market trends in materials for electronics and prepare technology and market forecasts as also techno-economic studies on selected areas of items on global basis and for Indian needs.

3.2.2 Set up library of books, periodicals, audio-visual aids, materials databases on R&D industry, natural resources, commerce etc., on both global as well as national basis enabling the Society to meet its objectives and effectively carry out its activities.

3.2.3 Disseminate relevant technical and commercial information in the form of papers, reports, books, monographs, journals in the areas of materials for electronics technology and organize study programmes, lectures, national and international conferences, training, programmes as well as exhibitions and similar promotional activities in this area.

3.2.4 Undertake, aid and promote publications of papers, journals, books, etc., on science, technology and applications of materials for electronics technology and allied subjects in Indian and foreign languages.

3.2.5 Set up research, design and development laboratories and conduct relevant R&D work up to pilot plant scale production as necessary, and undertake specific projects for system engineering and project engineering, to meet the objectives of the Society.

3.2.6 Develop or acquire and assimilate inputs from indigenous or foreign sources on materials science and engineering capabilities and progressively transfer the relevant knowledge/technology into manufacturing process utilizing resources from within the country and abroad, as necessary.

3.2.7 Engineer and set up pilot plants to produce small quantities of basic/processed materials with a view to:

a) Establish their production, technologies and techno-economic viability for large-scale production in the country.

b) Make available these items on prototype/minimum batch limited production basis for field trials, to promote usage and acceptance of these products by industry, to generate export market, to establish techno-commercial benefits from natural resources etc.

c) Partially, meet the critical and strategic needs of electronics industry to reduce their vulnerability on foreign sources to achieve self-reliance to enter in export market, to carry out research and development by other agencies etc.

d) Meet the needs of user industry where the items are not commercially available readily, and which are of the nature of

P. K. Sadanandan
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Centre for Materials for Electronics Technology
Indore, M.P., 452 001, India
spares of important components/systems not likely to be available due to strategic reasons or of discontinuity.

3.2.8 The Society may undertake jointly with, or subcontract to, various agencies relevant projects and strictly monitor them to achieve the objectives set out for such projects.

3.2.9 Transfer the technology developed to industry for commercial exploitation of the market and give further support for upgradation and improvement of the technology transferred by the Society.

3.2.10 The Society may also set up joint ventures within the country and in other countries, including developed countries, to develop export market for technologies and products develop through activities of the Society, as a promotion activity of the Society.

3.2.11 Be a repository of transfer of technology from foreign companies, laboratories and academic institutions engaged in development of new electronics materials with provision to set up joint development and if required, so as to achieve a – higher take-off point for its R&D within the country.

3.2.12 Initiate or take up as appropriate joint progress in materials standardization activities to develop and share knowledge and experience with educational, research and other R&D organizations in India and abroad, particularly in developing countries.

3.2.13 Set up National facilities, regional centers and other units in selected fields of specialization for carrying out experiments or for dissemination of knowledge.

3.2.14 Provide adequate opportunity to scientists and technical staff of the Society for promoting the scientific and technological excellence in the field of materials for electronics technology and related areas.

3.2.15 Set up efficient and reliable service facilities for materials characterization and certification, as a part of national programme in this field.

3.2.16 Develop by purchase, lease or otherwise in whole or in part, equipment and facilities to further the objects of the Society.

3.2.17 Establish maintain and manage facilities the acquisition, storage, retrieval, dissemination, evaluation, scrutiny and interpretation of information relating to the programme of the society.

3.2.18 Publish and disseminate the results of studies, research development, testing and evaluation conducted by the Society.

3.2.19 Register patents, designs and technical know-how that may be developed by the Society and transfer in full or any of the portion of
such patents/designs/technical know-how and to receive lump-sum, royalty and other such payment that the Society may decide on such patents, design and technical know-how.

3.2.20 Do all such other things, as the Society may consider necessary to the attainment of the main objects of the Society.

3.3 To manage the Society with an institutional framework, with independent and autonomous authority, responsibility and flexibility for dynamic and efficient operation so that:

3.3.1 Competent people from within the country as well as expatriate Indians abroad and others and encouraged and attracted to participate in the activities of the Society.

3.3.2 Mobility of staff from other organisations to the Society and back again is possible to ensure rapid build up of trained and skilled manpower to achieve the objective of the Society.

3.3.3 Special procedures are established to facilitate and enable smooth functioning of the Society and various activities including matters relating to personnel, finance, administration, purchase, travel, etc.

3.4 To receive grants, gifts, loans, subscriptions, donations or any other financial contribution in cash and securities and of any property whether movable or immovable from within the country and/or abroad including U.N. agencies, World Bank, bilateral and multilateral agencies subject to the prevailing laws of Govt. of India and to invest and deal with funds and moneys of the Society and to vary, alter or transfer such investments from time to time. The investment of C-MET funds shall be made as per section 11 sub section (5) of the Income Tax Act 1961. The activities of the proposed Society will be open to persons of either sex and whichever race, religion, creed, caste of class for the purpose of tax exemption to such subscribers and donors or not.

3.5 To purchase or take on lease or hire or otherwise acquire temporarily or permanently any movable or immovable property necessary or convenient for the furtherance of the objects of the Society.

3.6 To construct, maintain, alter, improve or develop any buildings or works necessary or convenient for the purpose of the Society.

3.7 To establish and maintain pension, provident and other funds for the benefit of the employee or for the purpose of the Society.

3.8 To do, get done, all such other lawful things, as are conducive or incidental to administration of the Society and attainment of other objectives.

P. L. Sadanandan
Registrar
Centre for Materials for Electronic Technology
Kaloor, P.O., Trivandrum-695 011.
4. All the incomes, earnings, movable and/or immovable properties including patents, designs, know-how etc. of the Society shall be solely utilized and applied towards the promotion of the objects only as set forth in this Memorandum of Association and no portion thereof shall be paid or transferred directly or indirectly by way of dividends, bonus, profit or any manner whatsoever to the members of the Society or to any person or persons claiming through any one or more of the members. No members of the Society shall have any personal claim on any movable and/or immovable properties of the Society or make a profit whatsoever by virtue of his membership. Notwithstanding anything contained herein above the Society shall not be debarred from making payment in good faith for recompensation to any member thereof or any other person in return for any services rendered to the Society or for traveling allowances, halting or other similar charges.

5. The names, addresses, occupations of the members of the Governing Council of the Society to whom by the Rules and Regulations of the Society, the management of its affairs is entrusted are as under:

<table>
<thead>
<tr>
<th>S.No.</th>
<th>Name &amp; Address</th>
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</table>
| 1)    | Shri Dayanidhi Maran  
         Hon’ble Minister  
         Ministry of Communications and  
         Information Technology  
         Electronics Nikelan, 6 CGO complex  
         New Delhi-110 003 |
| 2)    | Shri Brijesh Kumar  
         Secretary  
         Department of Information Technology  
         New Delhi – 110 003 |
| 3)    | Shri Ajoer Vidya  
         JS&FA  
         Department of Information Technology  
         New Delhi – 110 003 |
| 4)    | Dr. U.P. Phadke  
         Advisor (SG ‘G’) (R&D in Electronics)  
         Department of Information Technology  
         New Delhi – 110 003 |
5) Shri Pankaj Agrawala  
Joint Secretary (Societies)  
Department of Information Technology  
New Delhi – 110 003

6) Shri Nirmal Singh  
Director (C&I), Room No. 252  
Planning Commission  
Yojana Bhawan  
New Delhi – 110 001

7) Prof T.R.N. Kuttly  
Materials Research Centre  
Indian Institute of Science  
Bangalore – 560 012

8) Dr V.C. Soltni  
Head, Physics Group  
Bhabha Atomic Research Centre  
Trombay, Mumbai-400 085

9) Dr K.S. Sastri  
Deputy Director, PCM,  
ISRO, VSSC  
Thiruvanthapuram-695 022

10) Dr. M.J. Zarabi  
Chairman & Managing Director  
Semi-conductor Complex Ltd  
SAS Nagar- 160 059  
Mohali, Punjab

6. A copy of the Rules & Regulations of the Society, certified to be correct copy by three Members of the Governing Council is filed along with the Memorandum of Association.

7. We, the several persons, who names and addresses are given below, having associated ourselves for the purpose described in this Memorandum of Association, do hereby subscribe our names to this Memorandum of Association and set up several and respective names hereininto and form ourselves into a Society under this day of 1989 at Delhi.
<table>
<thead>
<tr>
<th>S. No.</th>
<th>Name, Occupations &amp; Address of Members</th>
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<tbody>
<tr>
<td>1.</td>
<td>Shri G. P. Srinivasan</td>
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<td></td>
<td>Chairman-cum-Managing Director</td>
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<td>M/s. Central Electronics/Ltd.</td>
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<td>4, Industrial Areas</td>
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<td>Sahibabad -201 010</td>
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<td>Shri A.K. Agarwal</td>
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<td>JS&amp;FA</td>
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<td>Department of Electronics</td>
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<td>New Delhi – 110 003</td>
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<td>Shri U.V. Warlu</td>
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<td>Marine &amp; Communication Electronics</td>
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<td>Kukatpally, Hyderabad - 500 702</td>
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<td>4.</td>
<td>Shri B.D. Jethra</td>
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<td>Acting Advisor (I&amp;M)</td>
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<td>Yojana Bhawan</td>
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<td>Tata Institute of Research</td>
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<td>6.</td>
<td>Prof. G.P. Sivastava</td>
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<td>Head,</td>
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<td>Department of Electronics Science</td>
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<td>Dr. B.K. Sarkar</td>
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<td>Central Glass &amp; Ceramics Research</td>
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<td>Institute</td>
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<td>Dr. S.G. Patil</td>
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<td>Director</td>
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<td>Department of Electronics</td>
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New Delhi-110 003

9. Prof. V.G. Bhide
   Physics Deptl
   University of Pune
   Pune – 411 007

10. Dr. M.J. Zarabi
    Executive Director
    Semi-Conductor Complex Ltd.
    SAS Nagar- 160 059
    Punjab

11. Shri R. Rajamani
    Secretary
    Department of Electronics
    New Delhi – 110 003

P. L. Sarma
Registrar
Centre for Materials for
Electronics Technology
Panachwadi, Off. Fashan Road,
Pune - 411 008
CERTIFIED that this is a true copy of the Memorandum of Association of the Society as amended in the 16th meeting of the General Body of the Society held on 19th October, 2004.

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<tr>
<th>S. No.</th>
<th>Full Name</th>
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<tr>
<td>1</td>
<td>Mr. Pankaj Agrawala</td>
<td>Joint Secretary (Societies),</td>
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<td>2</td>
<td>Dr. U.P. Phadke</td>
<td>Group Coordinator (R&amp;D in Electronics)</td>
<td>Department of Information Technology</td>
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<td>3</td>
<td>Shri Ajeer Vidya</td>
<td>Joint Secretary &amp; Financial Advisor</td>
<td>Department of Information Technology</td>
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